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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/667,046	09/21/2000	Steven A. Lytle	LYTLE 18	8375		
27964	7590 04/01/2003					
HITT GAINES & BOISBRUN P.C.			EXAMINER			
P.O. BOX 832 RICHARDSO			VU, HU	VU, HUNG K		
			ART UNIT	PAPER NUMBER		
			2811	·-		
			DATE MAILED: 04/01/2003	DATE MAILED: 04/01/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

		·	Un
	Application No.	Applicant(s)	
Advisory Action	09/667,046	LYTLE, STEVEN A.	
-	Examiner	Art Unit	-
	Hung K. Vu	2811	
The MAILING DATE of this communication app	ears on the cover sheet with the o	correspondence add	ress
THE REPLY FILED 24 March 2003 FAILS TO PLACE Therefore, further action by the applicant is required to final rejection under 37 CFR 1.113 may only be either: condition for allowance; (2) a timely filed Notice of Appelexamination (RCE) in compliance with 37 CFR 1.114.	avoid abandonment of this appli (1) a timely filed amendment wh	cation. A proper repict places the application of the contract	oly to a cation in
PERIOD FOR R	EPLY [check either a) or b)]		
a) The period for reply expiresmonths from the mailing			
b) The period for reply expires on: (1) the mailing date of this Ace event, however, will the statutory period for reply expire later to ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The dhave been filed is the date for purposes of determining the period of extensions of the shortener. 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortener.	han SIX MONTHS from the mailing date on S FILED WITHIN TWO MONTHS OF TH ate on which the petition under 37 CFR 1.1 Insion and the corresponding amount of the	f the final rejection. E FINAL REJECTION. S 136(a) and the appropriate e fee. The appropriate ext	See MPEP e extension fee ension fee under
(b) above, if checked. Any reply received by the Office later than three meanned patent term adjustment. See 37 CFR 1.704(b).	nonths after the mailing date of the final reju	ection, even if timely filed,	
1. A Notice of Appeal was filed on Appellant 37 CFR 1.192(a), or any extension thereof (37 CF			
2. \square The proposed amendment(s) will not be entered to	because:		
(a) they raise new issues that would require furth	her consideration and/or search	(see NOTE below);	
(b) \square they raise the issue of new matter (see Note	below);		
(c) they are not deemed to place the application issues for appeal; and/or	in better form for appeal by mat	erially reducing or s	simplifying the
(d) they present additional claims without cance	eling a corresponding number of	finally rejected clair	ns.
3. Applicant's reply has overcome the following rejection	ction(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	d be allowable if submitted in a s	eparate, timely filed	d amendment
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because: S		sidered but does NC	OT place the
6. The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	ecause it is not directed SOLELY	to issues which we	re newly
7. For purposes of Appeal, the proposed amendmen explanation of how the new or amended claims w			and an
The status of the claim(s) is (or will be) as follows	::		
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected: 21,24,25,29,30.			
Claim(s) withdrawn from consideration:			
8. \square The proposed drawing correction filed on is	s a) approved or b) disap	proved by the Exam	niner.
9. Note the attached Information Disclosure Statement	ent(s)(PTO-14 <u>49) P</u> aper No(s).	 .	
10. Other:	TOM THOMAS	1/2~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	
	SUPERVISORY PATENT EXAMIN TECHNOLOGY CENTER 2800		





Continuation of 5. does NOT place the application in condition for allowance because: the metal silicide 13 in Huang provides the interconnect between the layer 12 and the interconnect 44. The metal filled vias are also being used as interconnect metals. The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Huang teaches a portion of metal 44 filled the via 42a. The silicide layer 9 in Yoo provides the interconnect between the source/drain layer 5b, 7b and the interconnect 31. The metal filled vias are also being used as interconnect metals. The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Yoo teaches a

portion of metal 31,55 filled the via 27,51. Inohara et al. teaches, as shown in Figure 13, the silicide layer 25 is on the semiconductor surface 11 and the stopper film 13a located on the silicide layer 25. Note that the term "on" does not necessarily mean "directly on" and/or "directly contact". The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Inohara et al. teaches a portion of 16b filled the via 32.